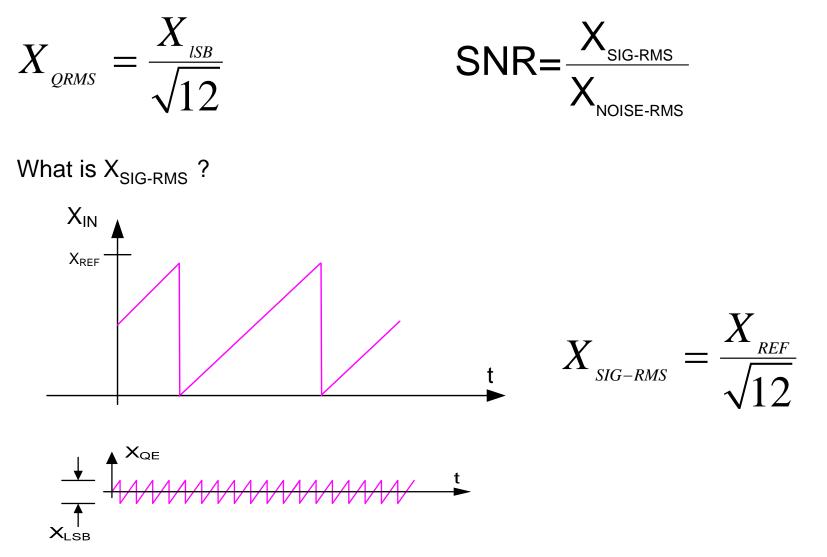
## EE 230 Lecture 42

#### Data Converters Nonideal Effects

#### Characterization of Quantization Noise

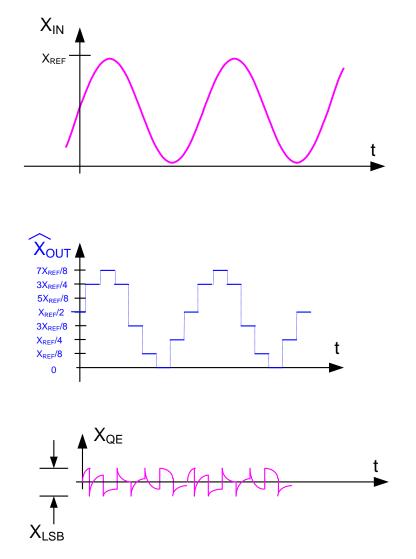
#### Saw tooth excitation



#### **Characterization of Quantization Noise**

Sinusoidal excitation

• Consider an ADC



Quantization noise is difficult to analytically characterize

Still need RMS value of  $X_{QE}(t)$ 

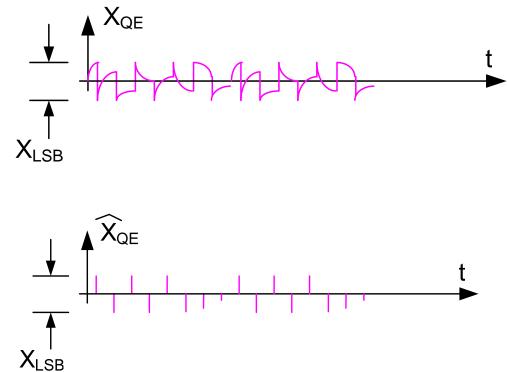
Will consider error in interpreted output

### **Characterization of Quantization Noise**

Sinusoidal excitation

• Consider an ADC

Will consider error in interpreted output



### Characterization of Quantization Noise

X<sub>QE</sub>

Sinusoidal excitation

• Consider an ADC (clocked)

Theorem: If n(t) is a random process, then

$$V_{\rm RMS} \cong \sqrt{\sigma^2 + \mu^2}$$

provided that the RMS value is measured over a large interval where the parameters  $\sigma$  and  $\mu$  are the standard deviation and the mean of <n(kT)>

This theorem can thus be represented as

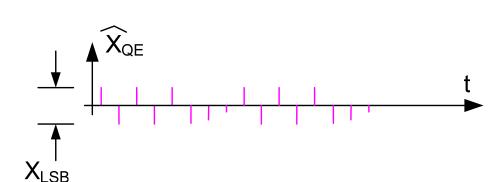
$$\mathsf{V}_{_{\mathsf{RMS}}} \cong \sqrt{\frac{1}{T_{_{L}}}} \int_{t_{_{1}}}^{t_{_{1}}+T_{_{L}}} n^{^{2}}(t) dt \cong \sqrt{\sigma^{^{2}} + \mu^{^{2}}}$$

where T is the sampling interval and  $T_L$  is a large interval

### Characterization of Quantization Noise

Sinusoidal excitation

• Consider an ADC



The quantization noise samples of the ADC output are approximately uniformly distributed between in the interval  $[-X_{LSB}/2, X_{LSB}/2]$ 

$$<$$
n(kT)> ~ U[-X<sub>LSB</sub>/2 , X<sub>LSB</sub>/2]

A random variable that is U[a,b] has distribution parameters  $\mu$  and  $\sigma$  given by

$$\mu = \frac{A + B}{2} \qquad \sigma = \frac{B - A}{\sqrt{12}}$$

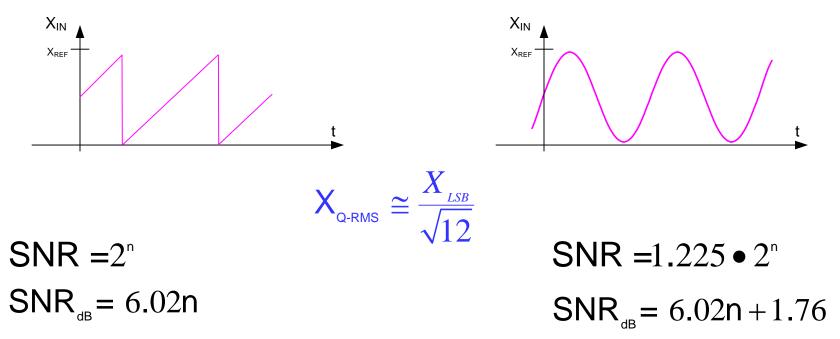
thus, the random variable n(kT) has distribution parameters

$$\mu = 0 \qquad \qquad \sigma = \frac{X_{LSB}}{\sqrt{12}}$$

### **Characterization of Quantization Noise**

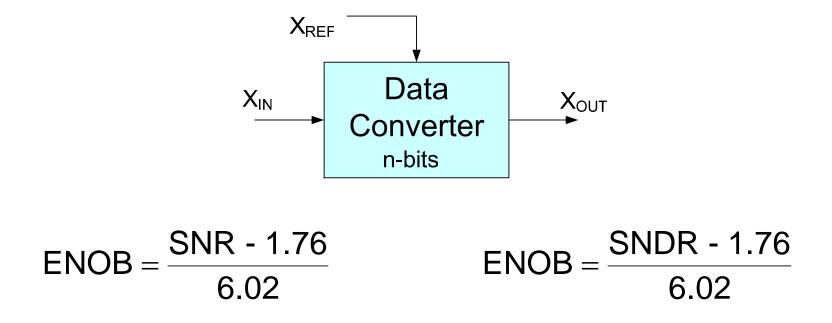
Saw tooth excitation

Sinusoidal excitation



Although derived for an ADC, same expressions apply for DAC SNR for saw tooth and for triangle excitations are the same SNR for sinusoidal excitation larger than that for saw tooth by 1.76dB SNR will decrease if input is not full-scale Equivalent Number of Bits (ENOB) often given relative to quantization noise SNR<sub>dB</sub> Remember – quantization noise is inherent in an ideal data converter!

#### Equivalent Number of Bits (ENOB)



These definitions of ENOB are based upon noise or noise and distortion

Some other definitions of ENOB are used as well – e.g. if one is only interested in distortion, an ENOB based upon distortion can be defined.

ENOB is useful for determining whether the number of bits really being specified is really useful

#### **Engineering Issues for Using Data Converters**

#### **1. Inherent with Data Conversion Process**

- Amplitude Quantization
- Time Quantization (Present even with Ideal Data Converters)

#### 2. Nonideal Components

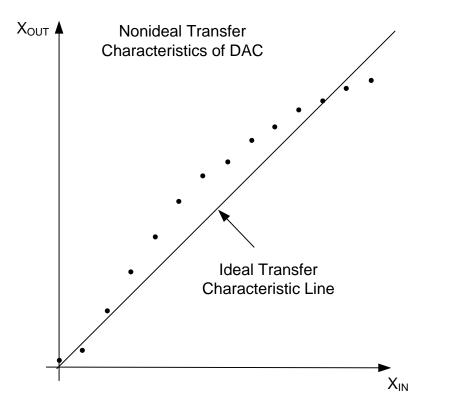
- Uneven steps
- Offsets
- Gain errors
- Response Time
- Noise

(Present to some degree in all physical Data Converters)

How do these issues ultimately impact performance?

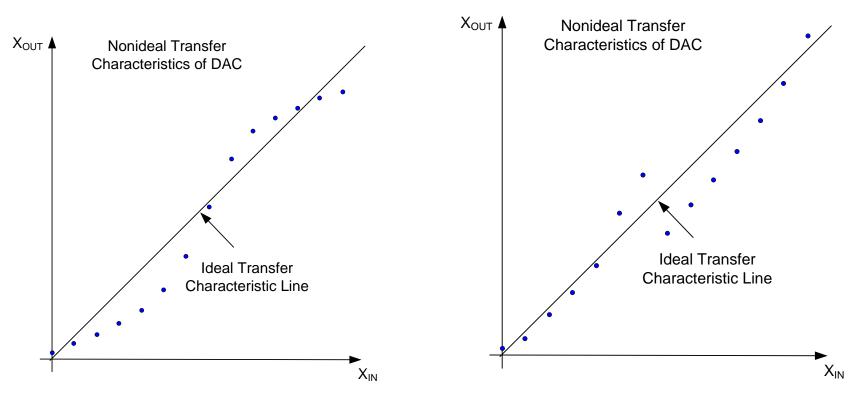
## **Nonideal Transfer Characteristics**

**Uneven Steps** 



## **Nonideal Transfer Characteristics**

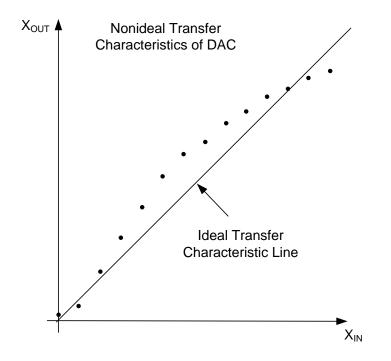
**Uneven Steps** 



Actual transfer characteristics can vary considerably from one device to another

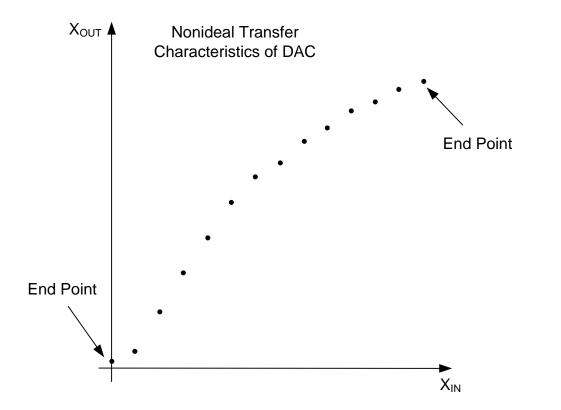
## **Nonideal Transfer Characteristics**

**Uneven Steps** 

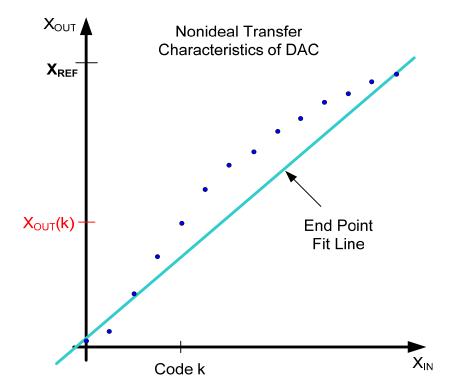


This is termed a nonlinearity in the data converter

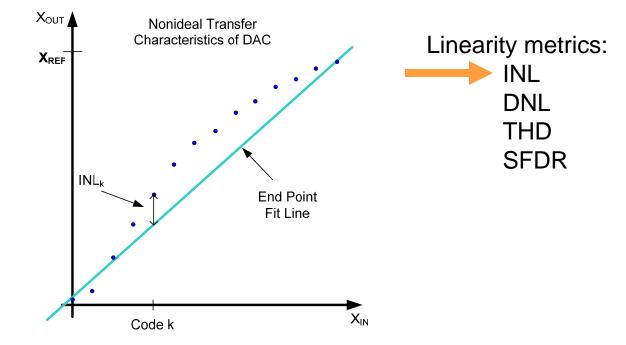
Linearity metrics (specifications) include INL, DNL, THD and SFDR



End points are the outputs at the two extreme Boolean inputs



#### End point fit line

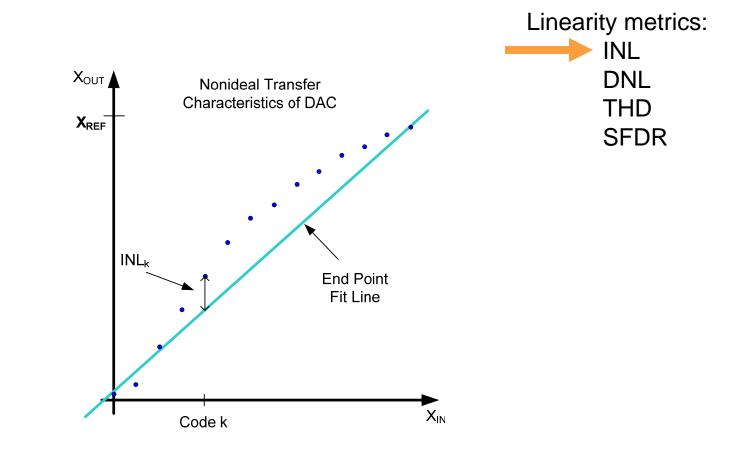


Integral Nonlinearity (INL)

Measure of worst-case deviation from linear

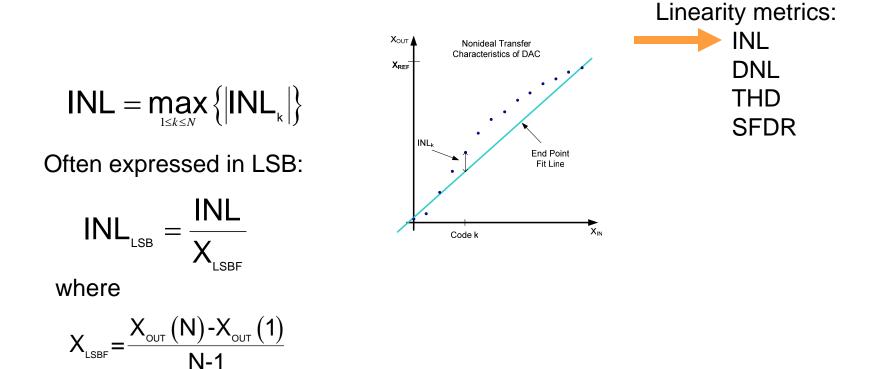
Define the INL at any input code k by:

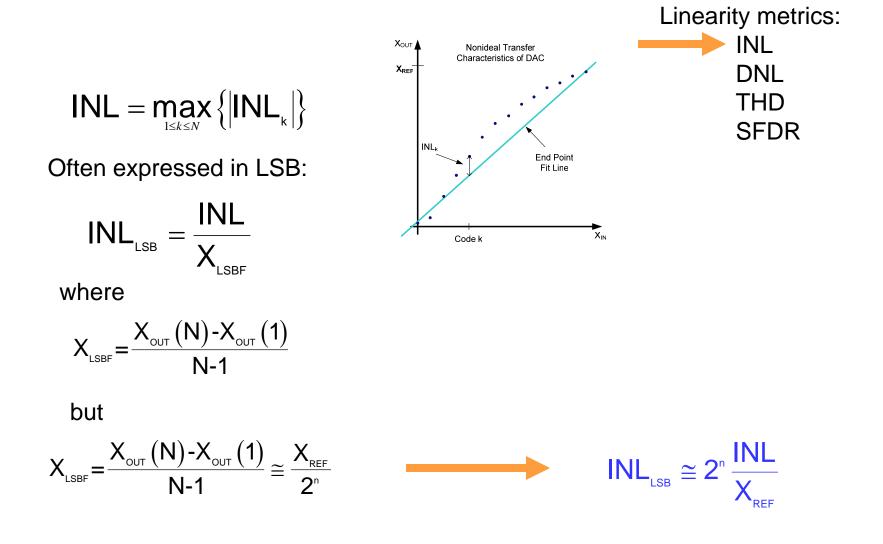
$$INL_{k} = X_{OUT}(k) - X_{FIT}(k)$$

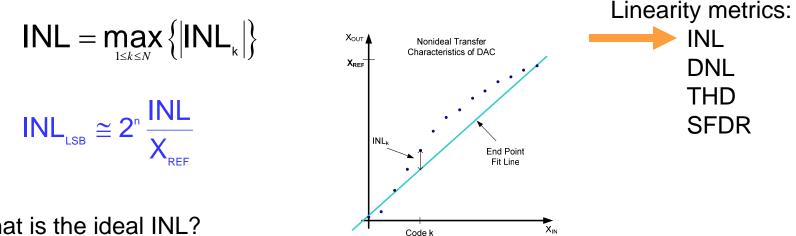


Define the INL by:

$$\mathsf{INL} = \max_{1 \le k \le N} \left\{ |\mathsf{INL}_k| \right\}$$







What is the ideal INL?

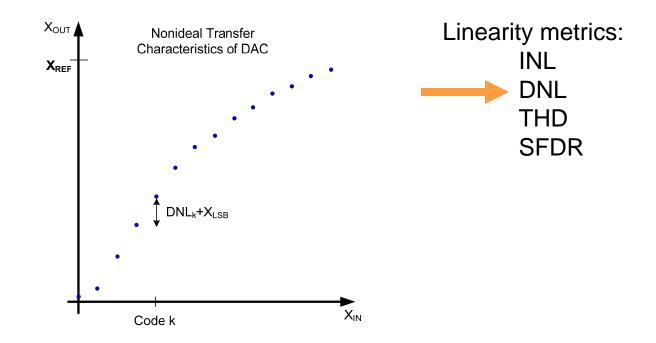
 $INL_{IDFAI} = 0_{ISB}$ 

What is an acceptable INL?

If INL<0.5LSB, it is <u>generally</u> considered acceptable

This would be the quantization error for an n-bit ADC What is the INL of a DAC?

> Varies from part to part, often close to 0.5LSB, occasionally better, but often worse - Given in Data Sheet



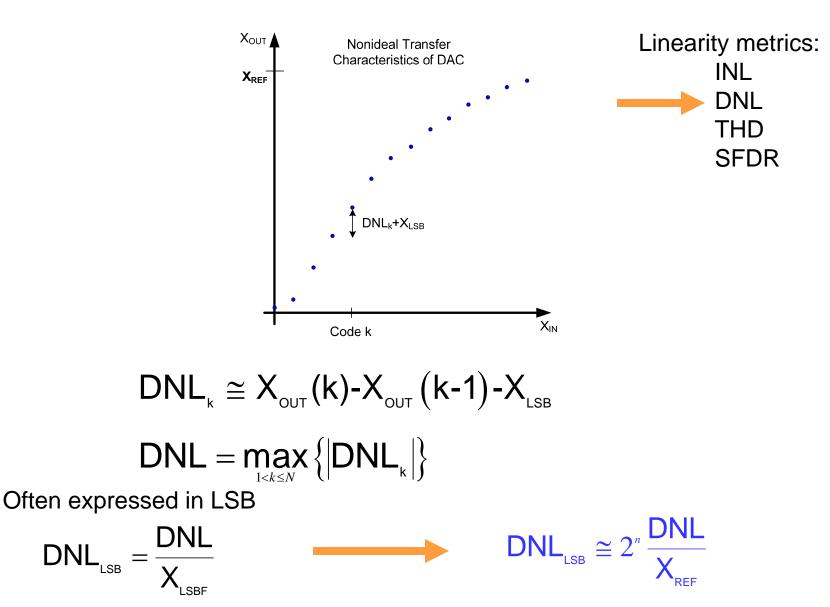
Differential Nonlinearity (DNL)

Measure of worst-case resolving capabilities

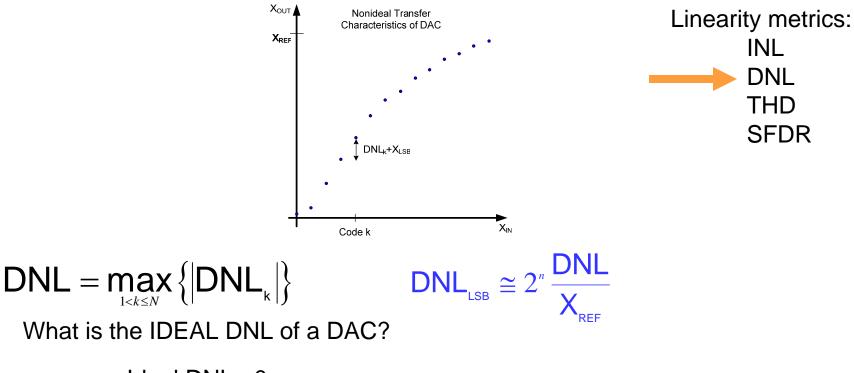
Define the DNL at any input code k by:

$$\mathsf{DNL}_{\mathsf{k}} = \mathsf{X}_{\mathsf{out}}(\mathsf{k}) - \mathsf{X}_{\mathsf{out}}(\mathsf{k}-1) - \mathsf{X}_{\mathsf{lsbf}} \cong \mathsf{X}_{\mathsf{out}}(\mathsf{k}) - \mathsf{X}_{\mathsf{out}}(\mathsf{k}-1) - \mathsf{X}_{\mathsf{lsb}}$$

## Differential Nonlinearity (DNL)



## Differential Nonlinearity (DNL)



Ideal DNL  $=0_{LSB}$ 

What is an acceptable DNL of a DAC?

If DNL<0.5LSB, it is <u>generally</u> considered acceptable What is the INL of a DAC?

Varies from part to part, often close to 0.5LSB, occasionally better, but often worse - Given in Data Sheet

# End of Lecture 42